

ADC AK5572 v1.1 11/2019

HPFE
SD
SLOW
DIF1
MSN
CKS0
CKS1
CKS2
CKS3

Control mode: R6
R6 unpop for HW mode, 0-ohm for I2C

Channel mode: R3
R3 unpop for stereo, 0-ohm for mono

3.3V0
GND

Designed by
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Short R1 and R2 in mono mode. Also unpop CH2 input stage until diff amp.

C47

R1

R2

C48

R53

Short R53 and R54 when not using XLRs.

R54

Tomi Nihtilä (nihtila.com)

ADC AK5572

Sheet:

File: ADC_AK5572.kicad_pcb

Size: A4

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KiCad E.D.A. kicad (5.1.4)-1

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