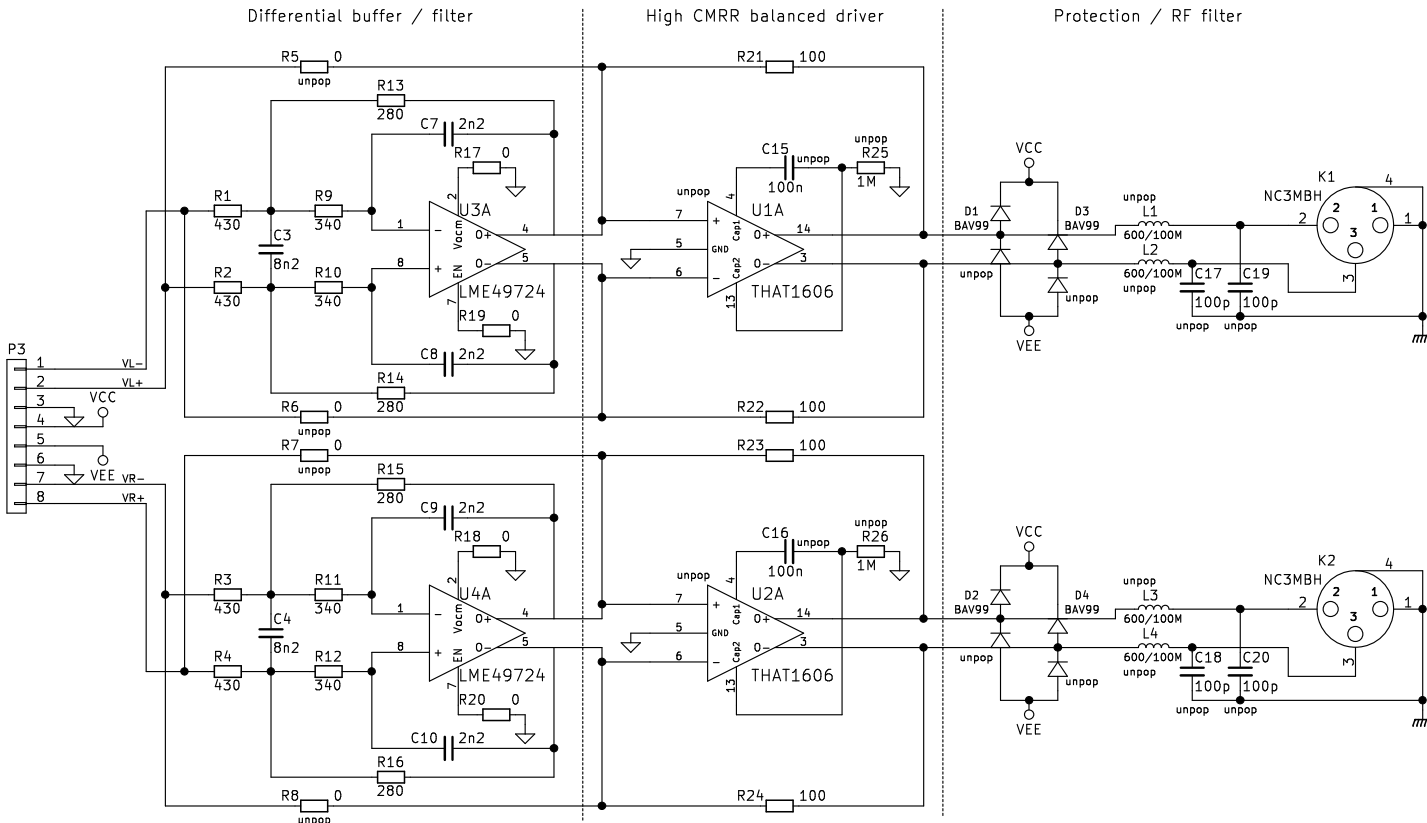


Addon BalOut



Differential buffer has a gain of -3.5 dB and f_c 86 kHz together with the filter in I/V-stage of H-DAC.

If you wish to change component values, consider the whole third order filter; also the H-DAC I/V-stage.

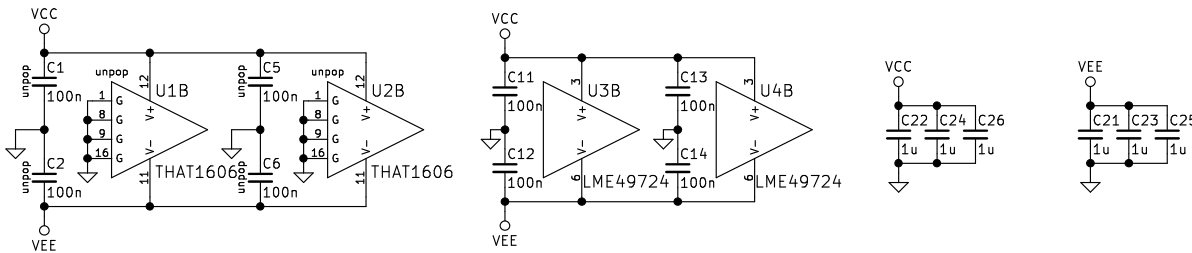
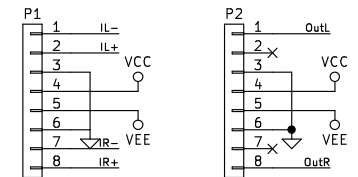
Note that THAT1606 has 6 dB gain

There are three routing options for balanced output:

- 1) Preferred option: Populate only differential buffers. Bypass balanced driver by placing 100 ohm resistors on R21-R24. This gives the best THD+N performance. Practical CMRR depends on component matching (and receiver stage). In home use weaker CMRR performance should not matter.
- 2) Populate differential buffers U3 and U4 and balanced drivers U1 and U2. Do not populate R5-R8 and R21-R24. This gives very good CMRR performance for long interconnects and hostile EMC environment. However, THD+N performance is weaker.
- 3) Populate only balanced drivers. Bypass differential buffers with R5-R8. The same as above but lacks low-pass filter.

Note: Do not connect signals directly to XLRs without any ICs as there is high common mode DC level.

Protection and RF filtering components are also optional.



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